

This Page Is Inserted by IFW Operations
and is not a part of the Official Record

BEST AVAILABLE IMAGES

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images may include (but are not limited to):

- BLACK BORDERS
- TEXT CUT OFF AT TOP, BOTTOM OR SIDES
- FADED TEXT
- ILLEGIBLE TEXT
- SKEWED/SLANTED IMAGES
- COLORED PHOTOS
- BLACK OR VERY BLACK AND WHITE DARK PHOTOS
- GRAY SCALE DOCUMENTS

IMAGES ARE BEST AVAILABLE COPY.

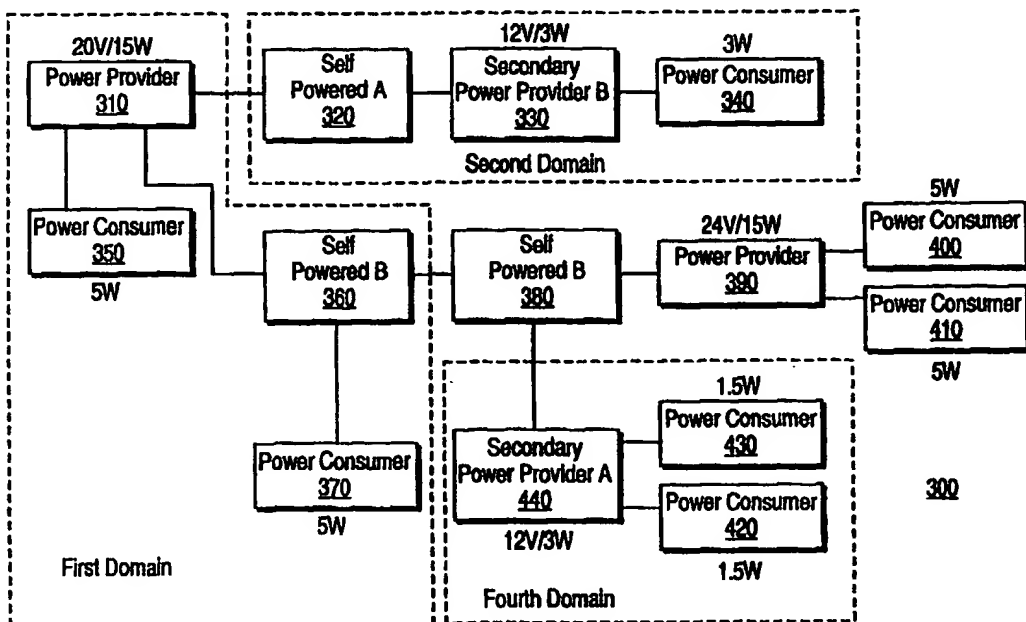
**As rescanning documents *will not* correct images,
please do not report the images to the
Image Problem Mailbox.**



INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification ⁶ : G06F 1/32		A1	(11) International Publication Number: WO 99/21074
			(43) International Publication Date: 29 April 1999 (29.04.99)
(21) International Application Number: PCT/US98/21811 (22) International Filing Date: 16 October 1998 (16.10.98) (30) Priority Data: 08/954,334 17 October 1997 (17.10.97) US (71) Applicant (for all designated States except US): INTEL CORPORATION [US/US]; 2200 Mission College Boulevard, Santa Clara, CA 95052 (US). (72) Inventor; and (75) Inventor/Applicant (for US only): BARD, Steven, R. [US/US]; 16510 N.E. 13th Street, Vancouver, WA 98684 (US). (74) Agents: TAYLOR, Edwin, H. et al.; Blakely, Sokoloff, Taylor & Zafman LLP, 7th floor, 12400 Wilshire Boulevard, Los Angeles, CA 90025 (US).		(81) Designated States: AL, AM, AT, AT (Utility model), AU (Petty patent), AZ, BA, BB, BG, BR, BY, CA, CH, CN, CU, CZ, CZ (Utility model), DE, DE (Utility model), DK, DK (Utility model), EE, EE (Utility model), ES, FI, FI (Utility model), GB, GE, GH, GM, HR, HU, ID, IL, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MD, MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SK (Utility model), SL, TJ, TM, TR, TT, UA, UG, US, UZ, VN, YU, ZW, ARIPO patent (GH, GM, KE, LS, MW, SD, SZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG). Published With international search report. Before the expiration of the time limit for amending the claims and to be republished in the event of the receipt of amendments.	

(54) Title: CIRCUIT AND METHOD FOR POWER DISTRIBUTION MANAGEMENT



(57) Abstract

Briefly, in accordance with one embodiment of the invention, a node includes: a circuit. The circuit (400) is configurable based, at least in part, on control signals external to the node to be applied to the node via a power distribution system (300) formed when the node is coupled to a plurality of nodes (320, 330, 340). The circuit includes the capability to deliver and to interrupt the delivery of power via the power distribution system.

FOR THE PURPOSES OF INFORMATION ONLY

Codes used to identify States party to the PCT on the front pages of pamphlets publishing international applications under the PCT.

AL	Albania	ES	Spain	LS	Lesotho	SI	Slovenia
AM	Armenia	FI	Finland	LT	Lithuania	SK	Slovakia
AT	Austria	FR	France	LU	Luxembourg	SN	Senegal
AU	Australia	GA	Gabon	LV	Latvia	SZ	Swaziland
AZ	Azerbaijan	GB	United Kingdom	MC	Monaco	TD	Chad
BA	Bosnia and Herzegovina	GE	Georgia	MD	Republic of Moldova	TG	Togo
BB	Barbados	GH	Ghana	MG	Madagascar	TJ	Tajikistan
BE	Belgium	GN	Guinea	MK	The former Yugoslav Republic of Macedonia	TM	Turkmenistan
BF	Burkina Faso	GR	Greece	ML	Mali	TR	Turkey
BG	Bulgaria	HU	Hungary	MN	Mongolia	TT	Trinidad and Tobago
BJ	Benin	IE	Ireland	MR	Mauritania	UA	Ukraine
BR	Brazil	IL	Israel	MW	Malawi	UG	Uganda
BY	Belarus	IS	Iceland	MX	Mexico	US	United States of America
CA	Canada	IT	Italy	NE	Niger	UZ	Uzbekistan
CF	Central African Republic	JP	Japan	NL	Netherlands	VN	Viet Nam
CG	Congo	KR	Kenya	NO	Norway	YU	Yugoslavia
CH	Switzerland	KG	Kyrgyzstan	NZ	New Zealand	ZW	Zimbabwe
CI	Côte d'Ivoire	KP	Democratic People's Republic of Korea	PL	Poland		
CM	Cameroon	KR	Republic of Korea	PT	Portugal		
CN	China	KZ	Kazakhstan	RO	Romania		
CU	Cuba	LC	Saint Lucia	RU	Russian Federation		
CZ	Czech Republic	LI	Liechtenstein	SD	Sudan		
DE	Germany	LK	Sri Lanka	SE	Sweden		
DK	Denmark	LR	Liberia	SG	Singapore		
EE	Estonia						

Circuit and Method for Power Distribution Management

5 BACKGROUND

1. Field of the Invention

The present invention relates to power distribution management, and more particularly, to power distribution management for devices coupled via a power cable
10 or bus, for example.

2. Background Information

The IEEE 1394-1995 specification (hereinafter "1394"), such as draft 8.0v3, approved December 12, 1995, available from the Institute of Electrical and Electronics Engineers (IEEE), describes a high-speed serial bus including the capability
15 to source power from one "node" or device to another over an intervening cable or power bus, such as in Annex A, Clause A.3.2. This cable power sourcing capability might be used, for example, to allow a notebook or desktop personal computer (PC) to provide operating power to an attached peripheral device, such as a camera or scanner. It might also enable a personal computer (PC) docking station to provide
20 operating power to a notebook computer docked via a 1394 specification compliant connection, as another example.

This capability introduces numerous complexities into the process of configuring the power source/sink relationships between a set of coupled devices, such as by a 1394 specification compliant connection, for example. A need,
25 therefore, exists for a circuit or technique for distributing power over power buses that integrates power management.

SUMMARY

Briefly, in accordance with one embodiment of the invention, a node includes
30 a circuit. The circuit is configurable based, at least in part, on control signals external to the node to be applied to the node via a power distribution system formed when the node is coupled to a plurality of nodes. The circuit includes the capability to deliver and to interrupt the delivery of power via the power distribution system.

BRIEF DESCRIPTION OF THE DRAWINGS

The subject matter regarded as the invention is particularly pointed out and distinctly claimed in the concluding portions of the specification. The invention, however, both as to organization and method of operation, together with objects, features and advantages thereof, may best be understood by reference to the following detailed description and read with the accompanying drawings in which:

FIG. 1 is a schematic diagram illustrating an embodiment of a circuit for power distribution management in accordance with the present invention;

FIG. 2 is a schematic diagram illustrating an embodiment of a power distribution system;

FIG. 3 is a schematic diagram illustrating an alternative embodiment of a circuit for power distribution management in accordance with the present invention.

DETAILED DESCRIPTION

The Universal Serial Bus (USB) specification, Version 1.0, available from the Universal Serial Bus Implementers Forum, Hillsboro, OR, is one example of a specification describing a signaling technique for coupling peripherals to a personal computer (PC). However, with continuing improvements in the performance and speed of microprocessors, a need for a high performance serial bus has been recognized. One example of a specification for such a high performance serial bus includes the previously referenced IEEE 1394-1995 specification ("1394"). One problem that has not been addressed in connection with such high performance serial buses, of which 1394 is one example, is how power is to be managed and allocated to the various nodes and ports that may provide and/or consume power via a power bus or cable linking or coupling the nodes.

For example, in terms of basic or functional blocks, it may be convenient to refer to power providers, power consumers and self-powered "nodes". Nodes may be intercoupled at V_p and V_g terminals, pins, or couplings which, taken together, in this context, comprise a "port", such as illustrated in FIG. 1, for example. A bus is created when one node port is coupled to another node port. Using these basic

descriptions, a need for a power distribution management mechanism may be illustrated.

FIG. 2 is a schematic diagram illustrating a power distribution system in which a multi-node bus is provided. Of course, a power distribution system may include the capability to distribute control signals, data signals, and power, as described in more detail hereinafter. As illustrated in FIG. 2, blocks 310 and 390 comprise power providers. Likewise, blocks 330 and 440 comprise secondary power providers. Blocks 350, 370, 420, 430, 410, 400 and 340 comprise power consumers and blocks 360, 320 and 380 comprise self-powered nodes. In this particular example, the total power to be delivered to the power consumers comprises 26 watts. However, existing implementations of power providers result in the power provider with the highest voltage providing all power to the bus. See, for example, IEEE 1394-1995, Annex A, clause A-4, Figure A-2. Therefore, in this example, the 24-volt, 15-watt power provider, 390, provides power to the bus. This power provider, therefore, does not provide sufficient power by 11 watts. Unless a power distribution management mechanism is devised, the example illustrated in FIG. 2 would result in a bus having non-operational nodes due to insufficient power, despite the presence of several power providers available to provide additional power to the bus.

FIG. 1 is a schematic diagram illustrating an embodiment of a circuit for power distribution in accordance with the present invention. It will, of course, be appreciated that the invention is not limited in scope to this particular embodiment. In actual use, such a circuit may be included in a node coupled to a power bus or cable as part of a power distribution system. In this context, "node" refers to a junction point in the power distribution and may, without limitation, include an electrical apparatus, such as a PC or peripheral, for example. As will be explained in more detail hereinafter, this particular circuit embodiment shown in FIG. 1 provides the ability for a port of the circuit to select a source from which power will flow into the node, decouple power from flowing to the node, or select a port from which to direct power to the bus. Likewise, it includes the capability to sink power from the power distribution system, source power to the power distribution system or operate as a power conduit between selected nodes.

This particular embodiment illustrates two ports; however, the invention is not limited to employing only two ports and any number of ports may be employed. As illustrated in FIG. 1, embodiment 100 includes power field effect transistors (PFETs), such as 110, 210, 180, 170, 130, 160 and 150. Likewise, embodiment 100 includes comparator 200 and inverting drivers 120 and 140. The embodiment also includes diode 190 and power regulator 205.

Embodiment 100 includes a variety of control signals that in actual use will most likely be externally derived with respect to the node. The circuit is configurable based, at least in part, on the externally derived control signals, which are to be applied to the node including the circuit when the node is coupled via a power distribution system, such as a power bus or cable, to form a plurality of nodes. Although the invention is not limited in scope in this respect, where a plurality of nodes are coupled to a power bus and these nodes all have different power consumption and power sourcing capabilities, for example, without limitation, software operating on a PC may be employed to provide the appropriate signals to the various nodes to configure the power source and sink attributes so that the previously described situation, in which insufficient power is provided, does not occur.

As will become clear later, the circuit embodiment illustrated includes the capability to deliver and to interrupt the delivery of power to each part of each node of the plurality of nodes, although the invention is not limited in scope in this respect. Likewise, although the power bus may comprise a 1394 specification compliant bus, the invention is not limited in scope in this respect either.

As illustrated in FIG. 1, embodiment 100 is coupled to two power ports, port A and port B, although the invention is not limited in scope in this respect. These ports may either receive or deliver power, as described in more detail hereinafter. As illustrated, ports A and B are coupled via PFET 110. Therefore, signal BRKPWR, when asserted, prevents power from either port A or port B being able to pass through to the other port, respectively. Likewise, signal ABPWRSEL operates as a selector signal with respect to power into the node via ports A and B. This signal is provided to both PFET 130 and, via inverter 120, to PFET 210. Therefore, when asserted, the power from port A is selected as the power provided into the node, but

when not asserted, the power from port B is selected. Of course, the invention is not restricted in scope to particular signaling inventions or choices, such as whether the signal is asserted or not asserted, for example.

Alternatively, signals BSRC and ASRC are employed when node local power is to be delivered out of ports A and B. In this context, local power refers to power provided by the node, as opposed to power originating from a source external to the node. Therefore, when BSRC is selected, power is delivered out of port B via PFET 180, whereas when port ASRC is selected, power is delivered out of port A via PFET 170. PWRSRC is coupled directly to PFET 150 and to PFET 165 via inverter 140. Therefore, when asserted, the nodes local power source is delivered as power input to power regulator 205 via PFET 150; however, when not asserted, power from either port A or port B is selected as the power input to power regulator 205 via PFET 140. Port A or B is selected in this embodiment-using signal ABPWRSEL.

Although not specifically illustrated in FIG. 1, the local power source, which is internal to the node, may be provided by a battery or the result of being coupled to an alternating current (AC) wall power source, for example.

"Power input" is a source of unregulated power to the nodes power regulator as delivered from the local power source or Port A or Port B. As illustrated, comparator 200 compares a voltage signal level from the local power source to the voltage signal level provided on one of the power ports, e.g., Port A or Port B. Therefore, signal PwrSrcSw is a signal that switches state when the voltage signal level on one of the power ports is greater than the local power source voltage signal level.

The operation of the embodiment illustrated in FIG. 1 may be described with reference to the power distribution system illustrated in FIG. 2. The circuit of FIG. 1 may be employed to establish independent power domains, as will be described in more detail hereinafter. Assume that each block/node illustrated in FIG. 2 includes the circuit illustrated in FIG. 1.

A first power domain is established by applying signals to self-powered block 320 to decouple from the power bus and, therefore, not receive power from power provider 310. This may be done by not asserting signal BRKPWR. This "breaks" or prevents the "pass through" of power from Port A to Port B of 320. In addition,

because the node being configured is self-powered, signal PWRSRC is asserted, resulting in the node's power regulator receiving power input from the local power source. The signals BSRC and ASRC are not asserted, removing the node's local power source as a source of power to ports A and B. Signal ABPWRSEL is in a
5 "don't care" state because the local power source has been selected as the power input to the node's power regulator. Likewise, block 380, which is also a self-powered block, is removed by non-assertion of signal BRKPWR in self-powered block 360 or 380 and similar treatment of signals PWR SRL, BSRC, and ASRC, as previously described. These particular blocks have three ports rather than two ports,
10 however, which may be in accordance with the embodiment illustrated in FIG. 3, for example. A first power domain has been created in this example. A 20 volt, 15 watt power provider, such as 310, has 5 watts of head room since the power consumers in this domain employ only 10 watts.

A second power domain is created as follows. Secondary power provider
15 block 330 is configured so that signal BRKPWR is not asserted. Likewise, PWR SRC is asserted so that the local power source is the power input to the node's power regulator and the source of power to the bus. BSRC is asserted, routing power from the local power source onto the bus via port B where 3-watt power consumer 340 will receive it. ASRC is not asserted, thereby preventing the local power source from
20 being distributed via port A. The node coupled to port A comprises self-powered block 320 and, therefore, may operate without receiving power via the power bus. Again, the state of signal ABPWRSEL is in a "don't care" state since the local power source has been selected.

The third power domain comprises blocks 380, 390, 400 and 410. This
25 domain has already been electrically isolated from the first domain. Secondary power provider block 440 in the fourth domain does not assert signal BRKPWR, thus preventing any power coming into port A of this node from being routed to any other port. Therefore, the third domain is electrically isolated.

The fourth domain is created by the creation of the third domain. Block 440
30 is configured to deliver power to power consumers 430 and 420 coupled to it. Signal BRKPWR of block 440 is not asserted, as described above.

Another feature of this embodiment in accordance with the present invention is that a node may be configured to consume power from the power bus when it provides power to the bus. Thus, an automatic switch from a local power source to the bus power source may occur if a higher voltage power provider is coupled to the power bus. For example, FIG. 3 is a schematic diagram showing a three-port embodiment in accordance with the invention. Referring to FIG. 3, signal SEL_PWR_SRC is not asserted, thereby selecting power from the power bus as the input power to the node's power regulator. Signals BRK_BC_PT and BRK_AB_PT are asserted, thus allowing power to pass from port A to B and port B to C. PRT_PWR_SEL is asserted as "10," thus distributing the node's power source power out to the bus through the diode shown and port C. Since power is passed through from ports A, B and C, if a power provider of a higher voltage than that of a predetermined threshold voltage of the node is coupled to any of ports A, B, and C, the voltage comparator will switch states, creating a signal, PwrSrcSw in this embodiment, that may be detected to signal removal of the node's local power source and power from the bus will continue to enter the node. Also, voltage from the bus will be prevented from back powering the node's local power source because of diode protection circuitry 490 in this embodiment.

An embodiment of a method of distributing power to a plurality of nodes coupled via a power distribution system, such as a power bus, may be accomplished as follows. For example, as previously described, a plurality of nodes may be coupled via a power bus, such as a serial data bus specification compliant power bus, such as 1394, as previously described. Likewise, externally derived control signals may be applied to each node coupled via the power distribution system. Based, at least in part, on the application of the previously described control signals, power may be delivered to selected nodes of the plurality of nodes. For example, as previously described, each node may include an embodiment of a circuit for power distribution management, such as one of the circuits illustrated in FIGs. 1 and 3, for example. As a result, as previously described, power may be delivered to selected nodes of the plurality of nodes. Likewise, power may be interrupted from being delivered to other selected nodes of the plurality of nodes, based at least in part on the applied control signals. As previously described, for example, the signal

BRKPWR, such as illustrated in FIG. 1, may be asserted. Likewise, power may be delivered to selected nodes, as previously described, for example, by not asserting control signal PWRSRC. Of course, the invention is not limited in scope to these particular embodiments or to these particular control signals.

5 In addition to power being interrupted from being delivered to other selected nodes based, at least in part, on the applied control signals, likewise, power may be interrupted from being delivered to selected ports of the other selected nodes based, at least in part, on the applied control signals. Again, in one particular embodiment, where power is delivered to selected nodes, power may also be delivered to selected
10 ports of selected nodes based, at least in part, on the applied control signals.

As previously described and illustrated, as a result of the applied control signals, independent power domains, such as illustrated in FIG. 2 and discussed in connection with the circuit embodiment illustrated in FIG. 1, may be formed. Likewise, although the invention is not limited in scope in this respect, in this
15 particular embodiment, the externally derived control signals comprise binary digital signals or bits. Furthermore, although this embodiment was illustrated with the 1394 specification, any serial data bus specification compliant power bus or, in fact, any power bus, even if it is not a serial data bus specification compliant power bus, may be employed.

20 While certain features of the invention have been illustrated and described herein, many modifications, substitutions, changes and equivalents will now occur to those skilled in the art. It is, therefore, to be understood that the appended claims are intended to cover all such modifications and changes as fall within the true spirit of the invention.

Claims:

1. A node comprising: a circuit; said circuit being configurable based, at least in part, on control signals external to said node to be applied to said node via a power distribution system formed when said node is coupled to a plurality of nodes; said circuit including the capability to deliver and to interrupt the delivery of power to said node via said power distribution system.
2. The node of claim 1, wherein said power distribution system includes a serial data bus specification compliant power bus.
3. The node of claim 1, wherein said node comprises a desktop personal computer (PC).
4. The node of claim 1, wherein said circuit includes the capability to operate as a power conduit between the nodes of said plurality.
5. The node of claim 4, wherein said circuit includes the capability to sink power from the power distribution system and source power to the power distribution system.
6. The node of claim 1, wherein each node of said plurality of nodes includes a circuit substantially similar to said circuit, said circuits including the capability to form independent power domains based on externally derived control signals to be applied to each of said nodes via the power distribution system.
7. The node of claim 1, wherein said circuit includes the capability to detect when the voltage of power being sunk to said node exceeds a predetermined voltage threshold of said node.
8. The node of claim 1, wherein said externally derived control signals comprise digital signals.
9. The node of claim 1, wherein said circuit further includes the capability to deliver and to interrupt the delivery of power each port of said node via said power distribution system.
10. A method of distributing power to a plurality of nodes coupled via a power distribution system comprising:
 - applying externally derived control signals to each node of the plurality of nodes;
 - delivering power to selected nodes of the plurality of nodes based, at least in part, on the applied control signals.
11. The method of claim 10, and further comprising:

interrupting the delivery of power to other selected nodes of the plurality of nodes based, at least in part, on the applied control signals.

12. The method of claim 11, wherein the delivery of power is interrupted to selected ports of other selected nodes based, at least in part, on the applied control signals.

13. The method of claim 12, wherein power is delivered to selected ports of selected nodes, based, at least in part, on the applied control signals.

14. The method of claim 13, wherein independent power domains are formed based, at least in part, on the applied control signals.

15. The method of claim 10, wherein power is delivered to selected ports of selected nodes based, at least in part, on the applied control signals.

16. The method of claim 10, wherein the power distribution system comprises a serial data bus specification compliant power bus.

17. The method of claim 10, wherein said externally derived control signals comprise binary digital signals.

18. A circuit comprising: a digital electronic circuit; said digital electronic circuit being configurable based, at least in part, on control signals external to said circuit to be applied to said circuit via a power distribution system formed when said circuit is used to couple a node to a plurality of nodes; said circuit including the capability to deliver and to interrupt the delivery of power to said node via said power distribution system.

19. The circuit of claim 18, wherein said power distribution system includes a serial data bus specification compliant power bus.

20. The circuit of claim 18, wherein said node comprises a desktop personal computer (PC).

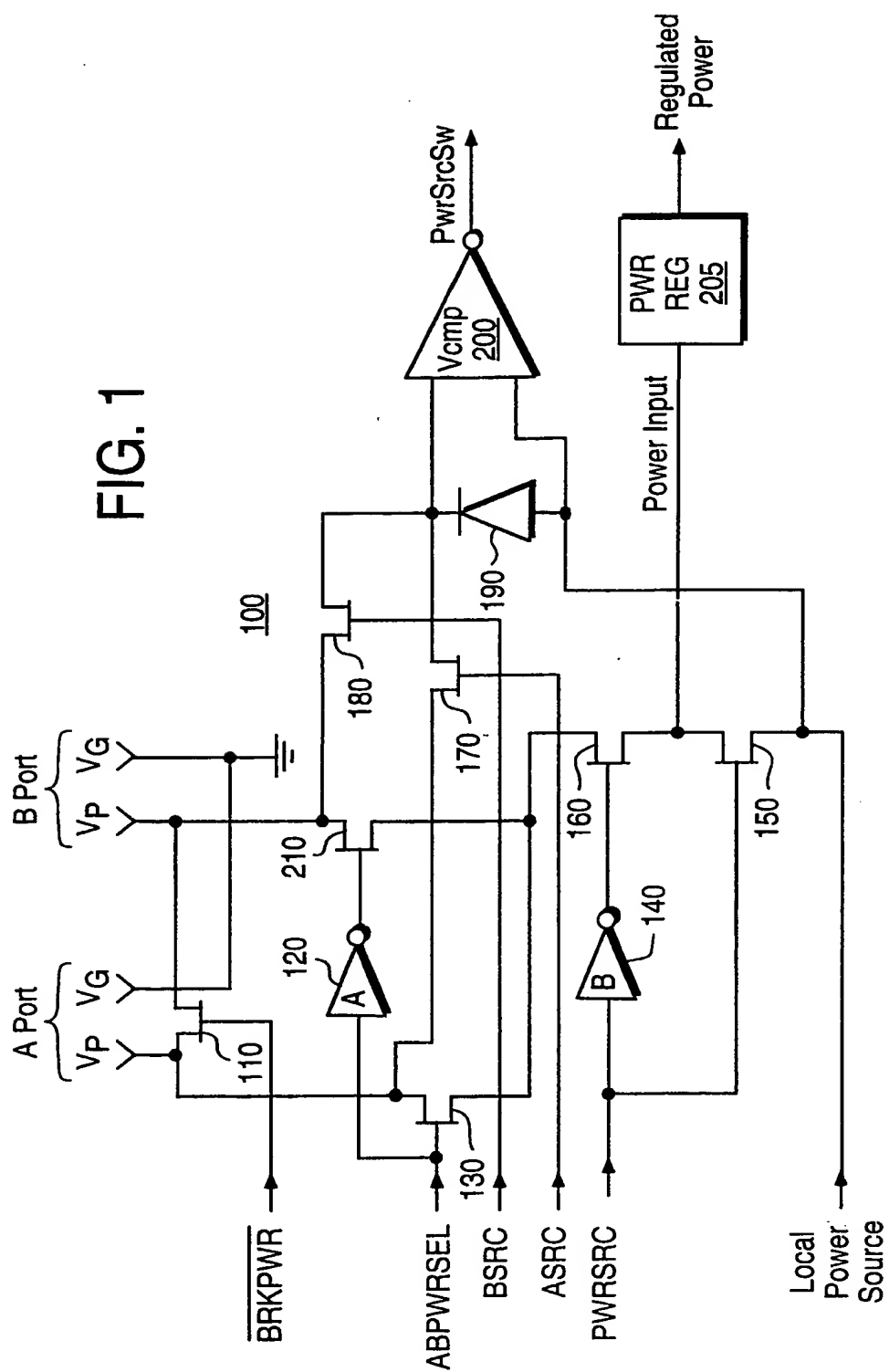


FIG. 2

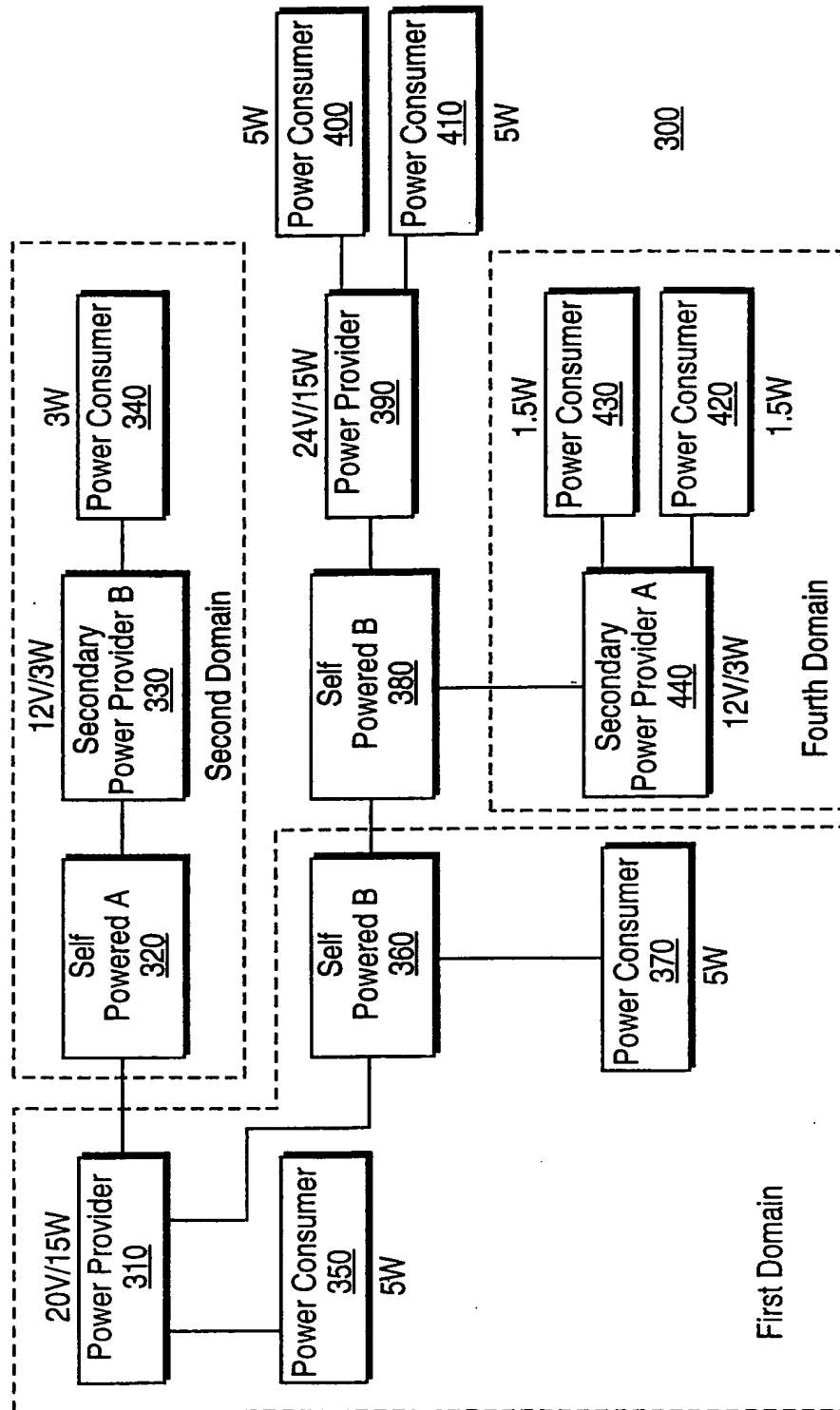
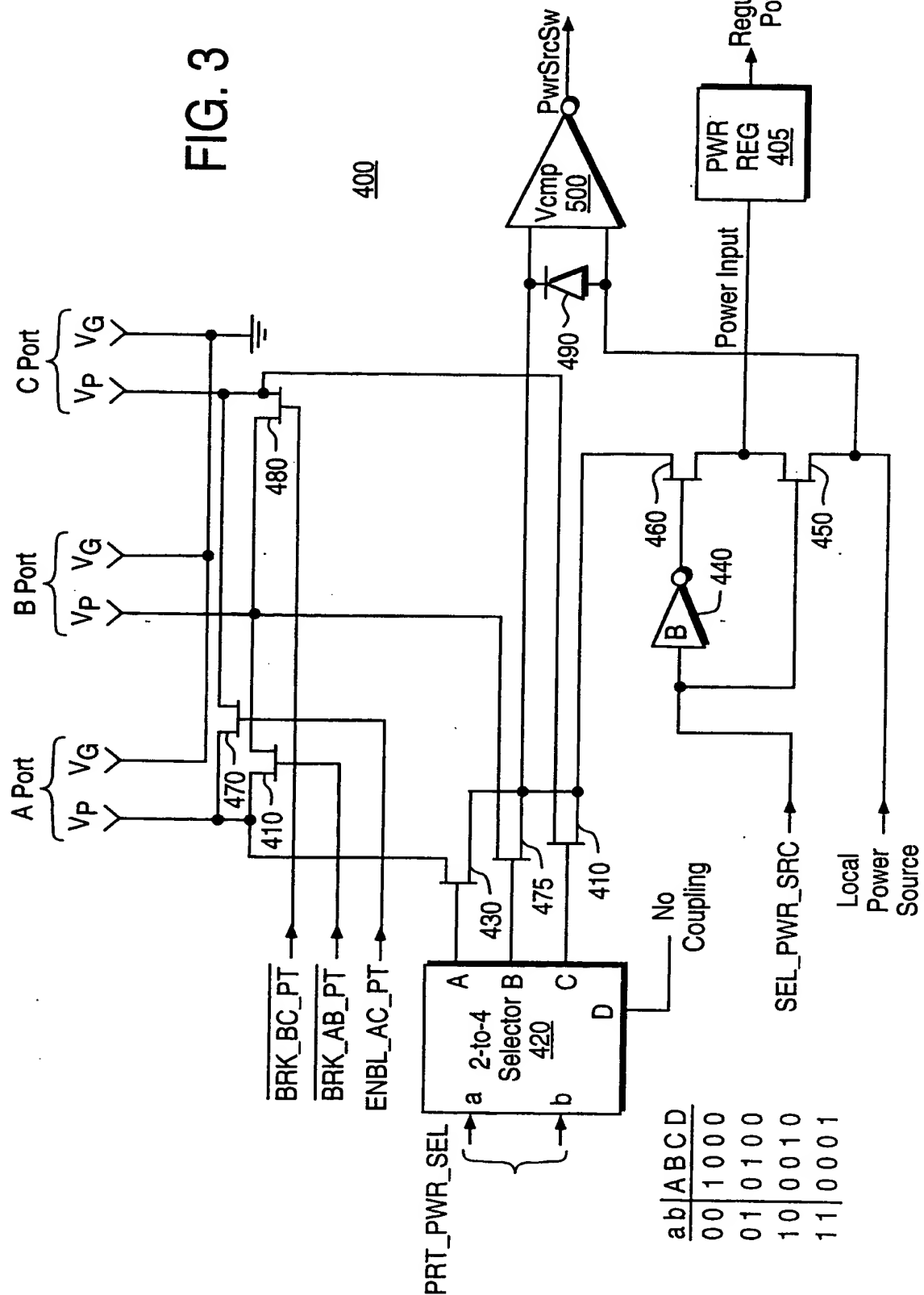


FIG. 3



INTERNATIONAL SEARCH REPORT

International application No.

PCT/US98/21811

A. CLASSIFICATION OF SUBJECT MATTER

IPC(6) : G06F 1/32

US CL : 395/750.01,750.03,750.08 :

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

U.S. : 395/750.01,750.03,750.08

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 4,659,942 A (VOLP) 21 April 1987, col. 2, lines 11-13 and 26; fig. 1, 11A-C); col. 3, lines 22-25 and 42; col. 6, lines 29-37 and 61-64;	1,4-7 and 9
Y		2,3,8,10-20
Y	US 4,897,842 A (HERZ et al) 30 January 1990, col. 4 lines 47-56	2,3,8,10-20
A	US 5,654,859 A (SHI) 05 August 1997	1-20
A	US 5,498,972 A (HAULIN) 12 March 1996	1-20



Further documents are listed in the continuation of Box C.



See patent family annex.

* Special categories of cited documents:	*T* later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
A document defining the general state of the art which is not considered to be of particular relevance	*X* document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
E earlier document published on or after the international filing date	*Y* document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art
L document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)	*A* document member of the same patent family
O document referring to an oral disclosure, use, exhibition or other means	
P document published prior to the international filing date but later than the priority date claimed	

Date of the actual completion of the international search

13 JANUARY 1999

Date of mailing of the international search report

22 FEB 1999

Name and mailing address of the ISA/US
Commissioner of Patents and Trademarks
Box PCT
Washington, D.C. 20231

Facsimile No. (703) 305-3230

Authorized officer

THERESA L. DAVIS

Telephone No. (703) 305-9648